

**REMARKS**

Claims 2-6, 8, 9, and 12-23 are pending in this application. By this Amendment, claims 2-6, 8, 9, 12, 14-18, 22, and 23 are amended and claim 11 is canceled without prejudice to or disclaimer of the subject matter found therein. No new matter is added.

Applicant appreciates the indication of allowability of claim 11 if rewritten in independent form including all the features of the base claim and any intervening claims. Applicant respectfully submits that all the claims are allowable for the reasons discussed herein.

Claims 2-6, 8, 9, and 11-15 are objected to because of minor informalities found therein. The amendments to the claims overcome the objection. The cancellation of claim 11 renders the objection moot. Thus, it is respectfully requested the objection be withdrawn.

Claims 8, 12, and 22 are rejected under 35 U.S.C. §112, second paragraph. The amendments to the claims overcome the rejection. Thus, it is respectfully requested the rejection be withdrawn.

Claims 2, 6, 12, and 14-16 are rejected under 35 U.S.C. §102(e) over U.S. Patent No. 6,430,235 to Shimoda; claims 3-5, 8, 9, and 13 are rejected under 35 U.S.C. §103(a) over Shimoda; and claims 17-23 are rejected under 35 U.S.C. §103(a) over Shimoda in further view of U.S. Patent No. 6,839,481 to White. The rejections are respectfully traversed.

Shimoda fails to disclose or suggest an optical interconnection circuit, comprising an integrated circuit chip; a first circuit block and a second circuit block provided on the integrated circuit chip, the first circuit block and the second circuit block including a plurality of elements each of which has a light emitting function or a light receiving function; an optical waveguide that is provided on the integrated circuit chip, the optical waveguide optically connecting the plurality of elements, a first element of the plurality of elements emitting a first light, a second element the plurality of elements emitting a second light, a

third element of the plurality of elements receiving the first light, a fourth element of the plurality of elements receiving the second light, a wavelength of the first light emitted by the first element being different from a wavelength of the second light emitted by the second element, the first circuit block including the first element, the second circuit block including the third element, the first circuit block or the second circuit block including the second element, and the first circuit block or the second circuit block including the fourth element, as recited in independent claim 16.

Further, the alleged combination of Shimoda and White does not disclose, teach or suggest an optical interconnection device, the device comprising a first light emitting element that emits a first light; a second light emitting element that emits a second light whose wavelength is different from a wavelength of the first light; and an optical waveguide that transmits the first light and the second light, as recited in independent claim 19.

As the Office Action admits, Shimoda does not disclose, "transmitting both the first and second lights along the same waveguide." Also, as the Office Action admits, the applicable prior art does not disclose, nor does it reasonably suggest an optical interconnection circuit having a plurality of circuit blocks disposed thereon optically connected via a waveguide so that a light signal (generated by a light emitter) from one circuit block will be transmitted to the other circuit block where it will be received (by a light receiver), and further having a plurality of micro-tiled shaped elements disposed on each of the circuit blocks for the purposes of transmitting and receiving optical signals of varying wavelength.

Hence, Shimoda does not disclose or suggest, at least, an optical waveguide that is provided on the integrated circuit chip, the optical waveguide optically connecting the plurality of elements, a first element of the plurality of elements emitting a first light, a second element the plurality of elements emitting a second light, a third element of the

plurality of elements receiving the first light, a fourth element of the plurality of elements receiving the second light, as recited in claim 16. Also, Shimoda does not disclose or suggest, at least, an optical waveguide that transmits the first light and the second light, as recited in independent claim 19.

Contrary to the Office Action assertion, White does not overcome the deficiencies of Shimoda as applied to claim 19 and similarly, to claim 16 because White is not prior art. White was filed in the United States on April 4, 2003. This application claims priority from JP 2002-355348 filed December 6, 2002, in Japan. An accurate English translation of priority document JP 2002-35548 is attached. Accordingly, Applicants have perfected priority to JP 2002-355348. Thus, White is not prior art to the claimed invention.

Thus, Shimoda fails disclose or suggest each and every feature of claims 16 and 19 and the 35 U.S.C. §102 rejection of claim 16 and 35 U.S.C. §103 rejection of claim 19 are inappropriate. Because Shimoda does not disclose or suggest the features of claims 16 and 19, Shimoda cannot anticipate or render obvious the subject matter of claims 2-9 and 12-18, which depend from claim 16, and the subject matter of claims 20-23, which depend from claim 19, for the reasons discussed with respect to claims 16 and 19 as well as the additional features recited therein. It is respectfully requested that the rejections be withdrawn.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims 2-6, 8, 9, and 12-23 are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



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Attachment:

Certified Translation of JP 2002-355348

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